

<b>Notice of References Cited</b>	Application/Control No. 10/692,773	Applicant(s)/Patent Under Reexamination LEE, HEON	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2005/0090056	04-2005	Lee, Heon	438/257
	B	US-2005/0090119	04-2005	Lee, Heon	438/720
	C	US-2004/0063223	04-2004	Costrini et al.	438/003
	D	US-6,627,913	09-2003	Chen, Gary	257/10
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 529, 532-33, 581.
	V	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 671-72.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.